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10/28/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application)

YAMADA *et al.*)

Application No. 09/982,839)

Filed: October 22, 2001)

For: A SEMICONDUCTOR DEVICE, A METHOD OF)
MANUFACTURING THE SAME AND STORAGE)
MEDIA)

Attorney Docket No. HITA.0112)

Art unit: 2824

Examiner:
LE, VU ANH

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

RECEIVED
OCT 16 2003
TECHNOLOGY CENTER 2800

RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

This is in response to the Office Action dated September 23, 2003, the shortened period for response to which will expire on October 23, 2003. Applicants hereby elect the continuing prosecution of Group I, drawn to a layout of a memory device, without traverse. Claims 1 to 9, 13 to 23 and 25 to 40 pertain to the elected group.